FSTD 1917

One week National Workshop on

RECENT TRENDS IN SEMICONDUCTOR IC DESIGN IN INDIAN PERSPECTIVE A HANDS-ON APPRO



FSTD 1929

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Vice - Chancellor. Osmania University.

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Coordinator

Mr. Md. Misbahuddin

Asst. Prof., Dept. of ECE, UCE, OU.

Co-coordinators

Dr. M. Shyam Sunder

Asso. Prof., Dept. of ECE, UCE, OU.

Dr. Satya Kamal C

EC Member, Vignana Telangana.

2nd to 7th December 2024



ORGANIZED BY

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING UNIVERSITY COLLEGE OF ENGINEERING (AUTONOMOUS) OSMANIA UNIVERISTY, HYDERABAD, INDIA.



VIGNANA TELANGANA

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IEEE CAS/EDS JOINT CHAPTER

Venue

Dept. of Electronics & Communication Engg., University College of Engineering(A), Osmania University, Hyderabad.

RESOURCE PERSONS

1. Prof V Ramgopal Rao.

Vice-Chancellor, Birla Institute of Technology & Science (Pilani, Hyderabad, Goa, Dubai & Mumbai)

2. Dr. Satya Gupta,

President, VLSI Society of India, Founder EPIC Foundation

3. Dr. Manish Kumar Hooda.

Director, Indian semiconductor Mission & Head, Technology development, SCL, Chandigarh

4. Prof Alex James.

Professor and Dean External linkages, Digital University Kerala

5. Prof. Srihari Rao,

Professor, Department of ECE, NITW, Telangana

6. Dr. Deepak Mishra.

Head & Associate Project Director, TDS-01, SAC. ISRO, Ahmedabad

7. Dr. Aiith S.

Sr. Director of Engineering, Intel Corporation, **Bangalore**

8. Mr. Rajesh Gupta,

Sr. Director and Country Head, AMS OSRAM

9. Dr. Krishna Kanth Avalur.

Director, Analog/Mixed-Signal R&D, AMS OSRAM, Hvderabad

10. Dr. Nanda Kishore Chavali.

Development Manager, MathWorks, Hyderabad, India.

11. Mr. CSS Sarma,

Founder & CEO, WnP Hyderabad

12. Mr. M. Manoj,

Scientist, Research Center of Imarat, India.

13. Mr. Venkata Reddy Kolagatla,

Scientist-D. C-DAC R&D. Bengaluru

14. Mr. Sanieev Srivatsav.

Founder & CEO, ZOICX Technologies, Hyderabad,

15. Dr. Balaji Jayaram,

Member of Technical Staff, Micron Technology, Hyderabad, India.

16. Mr. Rajasekhar Bochkar,

Sr. Manager, Application Engineering, Synopsis **FDA India Pvt Ltd**

17. Mr. Bhaskar Kakani,

Co-Founder & CEO, BITSILICA

Workshop Objective

The objective of the workshop is to explore the current challenges and opportunities in the Indian Semiconductor IC Design industry, providing insights into overcoming technical barriers such as process variability and power optimization while leveraging emerging trends like Al-driven design and edge computing for growth and innovation. The workshop includes practical, hands-on sessions, Case studies and real time applications led by the CDAC, CoreEL and our Team, focusing on the use of industry-standard tools, including Cadence and Synopsys, Digital and Analog Tools and advanced simulation techniques for design verification and validation.

Who Can Participate?

- Executives, Engineering and Researchers from Manufacturing, Service and Govt. Organizations including R&D Laboratories.
- 2. Faculty members of Engineering Colleges.
- 3. Students and Research Scholars from academic institutions and Technical institutions.

Fee Details

Industry Person: ₹5000/-,

Academic Faculty: ₹2500/-,

Students and Full-time Research Scholars: ₹1000/-.

<u>ACCOMMODATION</u>: On request Accommodation will be provided for few participants (first-come, first-served basis) in the campus on payment.

How to Register?

Google Form link:

https://forms.gle/vBKq1VjuKHzb7YVm8

UPI QR Code:



Payment Details:

Acc Name: HEAD DEPT OF ELEC AND COMMNCS ENGG Acc No.: 52198271467 Bank: State Bank of India Branch: Osmania University IFSC: SBINOO20071

Whom to Contact?

- Mr. Md. Toufeeg Ahmed, 7674845887
- Mrs. Radhika, 9642487722
- Mr. T.M Praneeth Naidu 9014240241
- email: aiicou2024@gmail.com
- For Further Details visit: www.aiicou.com

About the C2S Program

MeitY sanctioned a Prestigious Chip to Start-up (C2S) program to develop ADPLL for BLE applications and train Manpower in Semiconductor Design. It is a pleasure to inform you that the College is carrying out Research activities using full fledged VLSI CAD Tools and FPGA boards, funded by MeitY that leads to Marketable Products/Technology Transfers.

About the Department

The Department of Electronics & Communication Engineering (ECE), established in 1959 under the leadership of Professor K. K. Nair, has evolved into a prestigious teaching and research institution in India. The department is offering one UG programme B.E. (ECE) and Four PG Programs namely Digital Systems, Systems and Signal Processing, Microwave & Radar Engineering and VLSI & Embedded Systems and a PhD Program. The department has been in forefront for research and training activities in the areas of VLSI, Embedded Systems, Digital Signal Processing, Image Processing, Microwave Engineering and Modern Telecommunications. The department successfully completed various projects funded by different agencies such as World Bank, SWISS Development Corporation, DST, Meity, DRDO, ISRO, AICTE and UGC etc.

About VIBHA

Vignana Telangana, headquartered in Hyderabad and registered as The Vignana Telangana Society (VTS) under the Telangana Registration of Societies Act, 2001, operates across all districts of Telangana. In collaboration with Vijnana Bharati (https://vijnanabharati.org), VTS promotes a dynamic science movement with a Swadeshi spirit, bridging traditional and modern sciences, as well as natural and spiritual sciences. It organizes various programs to educate the public, particularly the younger generation, on Science, Technology, Engineering, and Mathematics (STEM). These initiatives involve collaboration with scientists from national research institutes, industry, and academic institutions.

About the College

The University College of Engineering has the distinction of being the oldest and the biggest among the Engineering Colleges of the Telangana State. It was established in the year 1929, eleven years after the formation of Osmania University. The college was the sixth Engineering College to be established in the whole of British India. The college moved to its present permanent building in the year 1947. The college was given autonomous status in 1994. The college offers undergraduate programs, twenty-five seven postgraduate programs which are aimed to match current industry needs and self-employment. It also offers research programs leading to PhD in six different departments.

About the University

Osmania University, established in 1917, is the seventh oldest in India, the third oldest in south India and the first to be established in the erstwhile princely state of Hyderabad. Through out its existence of over Ten decades, it has shown remarkable progress and sustained an integrated development of all faculties. It has significantly contributed to the academic and economic development of not only the region but also of the Country. Its alumni have distinguished themselves nationally and internationally in various spheres of life and are spread far and wide around the world.

The University has a vision of developing, enhancing, and improving the quality of human resources to meet the challenges of regional, national and global socioeconomic changes. It's mission is to achieve excellence in teaching and research and to create opportunities for the students to contribute to the national and regional development. Osmania University is re-accredited by the National Assessment and Accreditation Council (an Autonomous Institution of the University Grants Commission) as 'A+' Grade University.